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12/20/01

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10029571	FILING DATE 12/20/2001	CLASS 702	SUBCLASS 119	GAU 2857	EXAMINER TSA
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\*\*CONTINUING DATA VERIFIED:

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\*\* FOREIGN APPLICATIONS VERIFIED:

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PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no
ATTORNEY DOCKET NO NAK1-BQ83		
TITLE : Multilayer board in which wiring of signal line that requires tamper-resistance is covered by component or foil, design apparatus, method, and program for the multilayer board, and medium		
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)		

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Dwg.	Figs.Dwg.	Print Fig.
TERMINAL DISCLAIMER		Primary Examiner		
		PREPARED FOR ISSUE Application Examiner		
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